

FFT Based VLSI Digital One Bit Electronic Warfare Receiver

By Henry C. Chien-In

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VLSI IMPLEMENTATION OF PIPELINED FFT BASED ON SDC-SDF pipelined fast Fourier
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AT URBANA-CHAMPAIGN COORDINATED SCIENCE LAB F/B -

2.1.2 Electronic Warfare Systems. . 1. FIR digital filters the Fast Fourier Transform has many algorithmic realizations.

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IEEE Xplore Abstract - FFT based VLSI digital -

Some details are presented of a VLSI (1.2- μ m CMOS) digital array signal processor (DASP) which has been designed for applications in high-performance fast Fourier

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imple- menting a wideband electronic-warfare receiver Chien, M. Yajima, C based digital receiver has been designed for the